

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD AND APPARATUS FOR PERFORMING POWER ROUTING ON A VOLTAGE ISLAND WITHIN AN INTEGRATED CIRCUIT CHIP
Application Number :	
Date :	
First Named Applicant:	Bing Chen
Confirmation Number:	
Attorney Docket Number:	BUR920020141US1

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Submitted By:	Elec. Sign.	Sign. Capacity
Richard M. Kotulak Esq. Registered Number: 27.712	/s/	Attorney

Documents being submitted:	Files
us-assignment	BUR920020141US1efs-usassn.xml us-assignment.xsl us-assignment.dtd BUR920020141US1Assignment1.tif BUR920020141US1Assignment2.tif BUR920020141US1Assignment3.tif BUR920020141US1Assignment4.tif BUR920020141US1Assignment5.tif
us-request	BUR920020141US1efs-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920020141US1efs-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	BUR920020141US1Dec1.tif
us-declaration	BUR920020141US1Dec2.tif
us-declaration	BUR920020141US1Dec3.tif
us-declaration	BUR920020141US1Dec4.tif
us-declaration	BUR920020141US1Dec5.tif
application-body	BUR920020141US1Spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd BUR920020141US1FIGSsheet1of5.tif BUR920020141US1FIGSsheet2of5.tif

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Comments

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